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Bulletin Date: 3/26/2014		Bulletin Effective Date: 3/26/2014	
Title: Si53306-B-GM Unconnected Output Enable Pins Notification			
Originator: Len Staller		Phone: 512-532-5235	Dept: Marketing
Customer Contact: Kathy Haggar		Phone: 512-532-5261	Dept: Inside Sales
Bulletin Details			
Description: When the Output Enable (OE) pin on the Si53306 fan-out buffer is left UNCONNECTED (NC) the output clocks will now be enabled via a weak pull-up internal resistor. Please see the Si53306 datasheet for complete information.			
Reason: This change will allow customers to leave the OE pin unconnected or “floating” if they do not wish to disable the output clocks as indicated in the datasheet and enable the clock outputs. Previously, the output clocks will be disabled if the OE pin is left unconnected. Customers who have not left the OE pin unconnected or “floating” will <u>not</u> be affected by this improvement.			
Product Identification: The following part numbers are affected: Si53306-B-GM. Parts that are marked with a date code “345” or later (YEAR = 2013, WW = 45) with the new weak pull-up enabled as described in “Description” above. Please see the datasheet “Top Marking Explanation” for the date code mark description.			
This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy. Please contact your local Silicon Labs sales representative with any questions about this notification. A list of Silicon Labs sales representatives may be found at www.silabs.com			
Customer Actions Needed: If and only if the OE is unconnected or “floating”, customers may choose to either: (1) Connect the OE pin to a logic ‘1’ or other hardware input control, or (2) Exchange Si53306 parts for updated parts with internal weak pull-up enabled			